

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Mark Boike et al.

APR 27 2005  
SC40  
PATENT & TRADEMARK OFFICESerial No.: 09/975,677Group Art Un 2183Filed: October 11, 2001Examiner: Coleman, EricFor: Integrated Circuit Containing  
Multiple Digital Signal ProcessorsAtty Docket: 4028-00800 / 01-373

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Sir:

LSI Logic Corporation, a Delaware corporation, certifies that it is the assignee of the entire right, title and interest in the patent application.

The undersigned has reviewed all the documents in the chain of title of the patent application identified above and, to the best of the undersigned's knowledge and belief, title is in the assignee identified above.

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I hereby declare that all statements made herein of my own knowledge are true, and that all statements made on information and belief are believed to be true; and further, that these statements are made with the knowledge that willful false statements, and the like so made, are punishable by fine or imprisonment, or both, under Section 1001, Title 18 of the United States Code, and that such willful false statements may jeopardize the validity of the application or any patent issuing thereon.

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Respectfully submitted,

  
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13:02

4084337460 LSI LOGIC → 917037464000

NO. 336 D004

PTO/SB/122 (09-04)

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Application Number	09/975,677
Filing Date	October 11, 2001
First Named Inventor	Mark Boike et al.
Art Unit	2183
Examiner Name	Coleman, Eric
Attorney Docket Number	01-373 / 4028-00800

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